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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Nicola Telecco

PATENT APPLICATION

Serial No.: 10/666,324

Group Art Unit: 2838

Filed: September 17, 2003

Examiner: G.L. Laxton

For: DUAL STAGE VOLTAGE REGULATION CIRCUIT

Response to Restriction Requirement

Hon. Commissioner for Patents  
Alexandria, VA 22313

Dear Sir or Madam:

In response to the Office action mailed December 17, 2004, the Examiner has required a restriction between Embodiment I - Fig. 1 and Embodiment II - Figs. 2 and 3.

Applicant respectfully wishes to point out that Figs. 2 and 3 are an extension or continuation of Fig. 1 and not a separate embodiment. For example, transistor 47 is seen to the left of Fig. 2. This is the same transistor 47 which is seen at the right of Fig. 1. The charge pump of Fig. 2 is an exemplary utilization circuit for the voltage regulator of Fig. 1. Accordingly, the proper characterization of the figures is as follows:

Figs. 1 and 2 (as well as Fig. 3) depict a combination of a voltage regulator and an exemplary utilization circuit, respectively. Fig. 1 is the subcombination of just the voltage regulator. Claims 16-20 read on the combination of voltage regulator and charge pump. Claims 1-15 read on the voltage regulator of Fig. 1, without any utilization circuit, such as the charge pump illustrated in Fig. 2.

Applicant elects to proceed on the subcombination claims 1-15, as shown and described with reference to Fig. 1, and requests that claims 16-20 be withdrawn from consideration.

This election is made without traverse.

CERTIFICATE OF MAILING

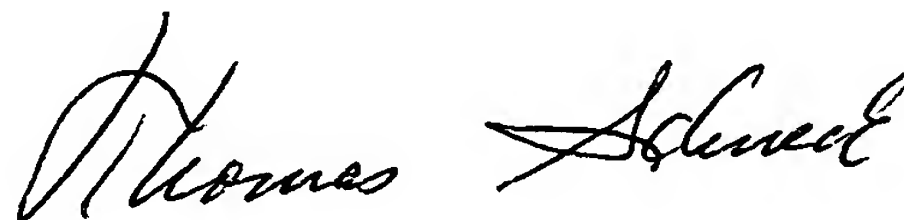
I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313

Signed: \_\_\_\_\_

Typed Name: Sally Azevedo

Date: January 7, 2005

Respectfully submitted,



Thomas Schneck

Reg. No. 24,518

P.O. Box 2-E

San Jose, CA 95109-0005

(408) 297-9733